

EAST - [Default EAST Workspace 1600x1200.wsp:1]

File View Edit Tools Window Help

Search, Add, Browse, Query, Star

DB: USPGPUB:USPAT:USOCRIEPO:JPO:DERWENT:BMSTDB

Default operator: OR Highlight all hit terms initially

☐ Pending

☒ Active

- L1: (477991) power near2 (sav\$3 or reduc\$4 or low\$2)
- L2: (26625) mode\$1 near10 1
- L3: (1045) noise same 2
- L4: (2815854) count\$3 or samp\$3
- L5: (478) 3 and 4
- L6: (355269) multiplier or multiply or multiplying or multiplication
- L7: (150) 5 and 6
- L9: (49546) (switch\$3 or select\$3) with 1
- L10: (79) 7 and 9
- L11: (65) 10 and sampling
- L12: (6743) (switch\$3 or select\$3) with 2
- L13: (55) 7 and 12
- L14: (43) 13 and sampling
- L15: (25522) (mode or modes) near10 1
- L16: (31) 14 and 15
- L17: (24) 13 not 16
- L18: (24) 10 not 13
- L19: (323353) power near2 (manag\$3 or control\$4)
- L20: (79127) (1 or 19).ti.
- L21: (157) 1 same 3 and 20
- L22: (18) 21 and 6
- L24: (181) 6.ti. and 20

☐ Failed

- (switch\$3 or select\$3) with1

☒ Saved

☒ Favorites

BRS form IS&R form Image Text HTML

	U	1	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrie	Inventor	S	C	P	3	
1	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 5675808 A	19971007	227	Power control of circuit modules	713/322	713/321		Gulick; Dele E. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
2	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 5467042 A	19951114	108	Low power clocking apparatus	327/293	326/93		Smith; Stephen A. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
3	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 5677849 A	19971014	9	Selective low power clocking ap	713/300	713/321		Smith; Stephen Arthur	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
4	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 5722086 A	19980224	8	Method and apparatus for redu	455/561	455/570		Teitler; Nicole D. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
5	<input type="checkbox"/>	<input checked="" type="checkbox"/>	EP 583120 B	19990915	22	Power controller to multiplying d				ASGHAR, S M et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
6	<input type="checkbox"/>	<input checked="" type="checkbox"/>	EP 938043 A2	19990825	5	Low power multiplier for CPU a				NICOL, CHRISTOPHER	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
7	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6065032 A	20000516	5	Low power multiplier for CPU a	708/628			Nicol; Christopher John	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
8	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6604120 B1	20030805	28	Multiplier power saving design	708/631	708/628		De'Angel; Edwin	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input checked="" type="checkbox"/>

Hit Details HTML

Ready NUM